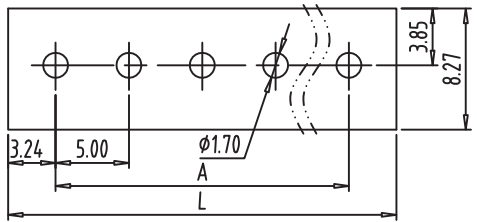
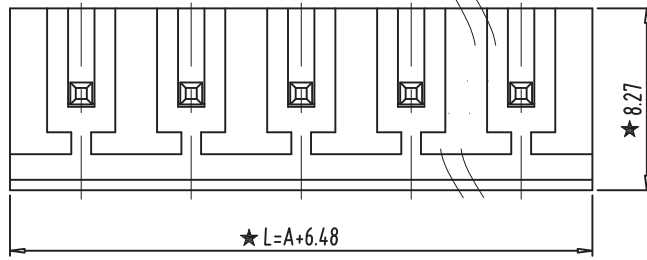


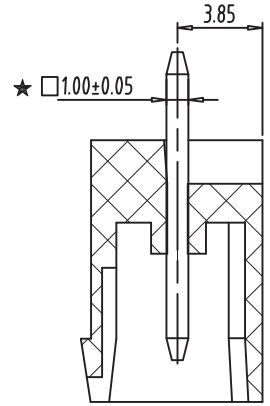
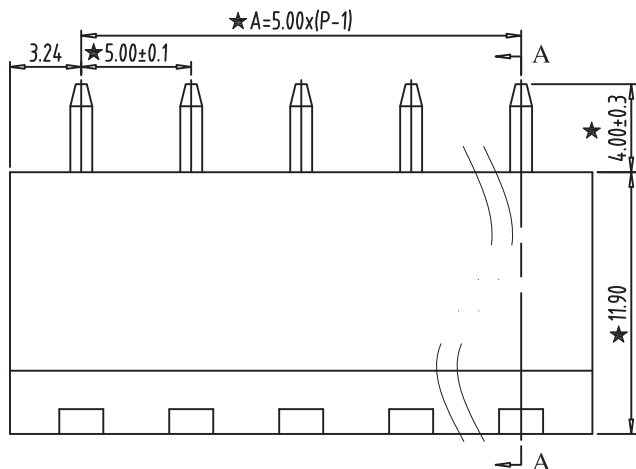
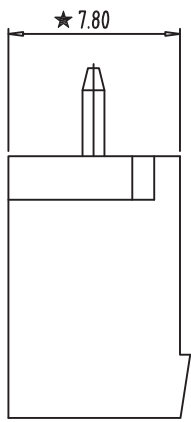
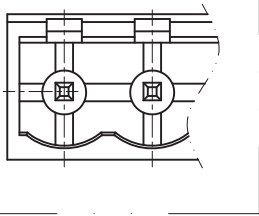
REV	CONTENT	CHK	DATE
V0.0	CONTENT	SK	2014/08/19



PCB LAYOUT
S=2:1



- NOTES:
1. Rating:300V 15A ;
 2. Insulation Withstands Voltage: AC2000V/MIN
 3. Insulation Resistance: 500MΩ or more at DC500V
 4. Using Temperature Range: -40°C~+105°C
 5. "★" is the important dimension
 6. RoHS compliance



SECTION:A-A

DIM	P	2	3	4	5	6	7	8	9	10	11	12
L		11.48	16.48	21.48	26.48	31.48	36.48	41.48	46.48	51.48	56.48	61.48
A		5	10	15	20	25	30	35	40	45	50	55
DIM	P	13	14	15	16	17	18	19	20	21	22	
L		66.48	71.48	76.48	81.48	86.48	91.48	96.48	101.48	106.48	111.48	
A		60	65	70	75	80	85	90	95	100	105	

Total Pitch Tolerances in mm												Nominal dimension range in mm												⊥	≡	—	//		
over 0	over 30	over 53	over 70	over 90	over 115	over 150	over 250	over 350	over 0	over 30	over 53	over 70	over 90	over 115	over 150	over 200	over 250	over 0	over 10	all	over 0	over 100	over 150	0.2	0.3	0.3	0.2	0.3	0.5
to 30	to 53	to 70	to 90	to 115	to 150	to 250	to 350	to 500	to 30	to 53	to 70	to 90	to 115	to 150	to 200	to 250	to 320	to 10	to 10		to 100	to 150							

ITEM	NAME OF PART	NO. OF PART	Q'TY	MATERIAL	NOTE
DWG.	SK	DATE	2014/08/19	SIZE	A4 UNITS mm SHEET/OF 1/1
CHK.		DATE		SCALE	3:1
APP.		DATE		NAME	KSE-2EDGVC-5.0-XX
KEEN SIDE ELECTRONICS				DWGNO.	