



Customer:

LCD MODULE

MODULE NO.:

KSECB2004XXXX-R01 SERIES

Approved by:		
Approved by	Checked by	Prepared by

RECORDS OF REVISION

Part Number	Revision	Revision Content	Revised on
KSECB2004XXX-R01	1.0	First issue	Oct. 12th, 2022

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1. MODULE CLASSIFICATION INFORMATION

KSE C B 2004 X X X - R 01

(4)

① KSE: COMPONET, Ltd

2

② C: Character Type, G: Graphic Type

(3)

③ B: COB, G: COG

(1)

4 Display Font: 20 * 4

5 LCD Mode: $B \rightarrow STN$ -Blue Negative $F \rightarrow FSTN$ Positive

(5) (6) (7) (8) (9)

 $G \rightarrow STN$ Gray Positive $Y \rightarrow STN$ Yellow Green Positive

⑥ Backlight Type: $N \rightarrow Without backlight$ $A \rightarrow Amber LED backlight$

 $B \rightarrow Blue LED$ backlight $G \rightarrow Green LED$ backlight

R→ Red LED backlight W→ Withe LED backlight

Y→ Yellow-Green LED backlight

7 LCD Polarizer Type/Temperature range/View direction :

D→ Transflective, W.T, 12:00 E→ Transmissive, W.T, 6:00

P→ Reflective, W. T, 6:00 Q→ Transmissive, W.T, 12:00

Z→ Transflective, W.T, 6:00

(8) Character Bank:

A→ English / Japan B→ English / European

R→English / Cyrillic / Portuguese / Russian

 $T \rightarrow English / Russian$ $G \rightarrow Hebrew$

9 Model serials no.:

2. FUNCTIONS & FEATURES

• KSECB2004XXX-R01 Series LCD type:

• Display Contents : 2 0 * 4 Characters (5*8 dots)

Driving Scheme : 1/16Duty; 1/5Bias
 Viewing Direction : 6 o'clock or 12 o'clock
 Driver IC : AIP31066L-002

• Interface : Parallel

• Operating Temperature :- $20^{\circ}\text{C} - + 70^{\circ}\text{C}$ • Storage Temperature :- $30^{\circ}\text{C} - + 80^{\circ}\text{C}$

• RoHS Compliant

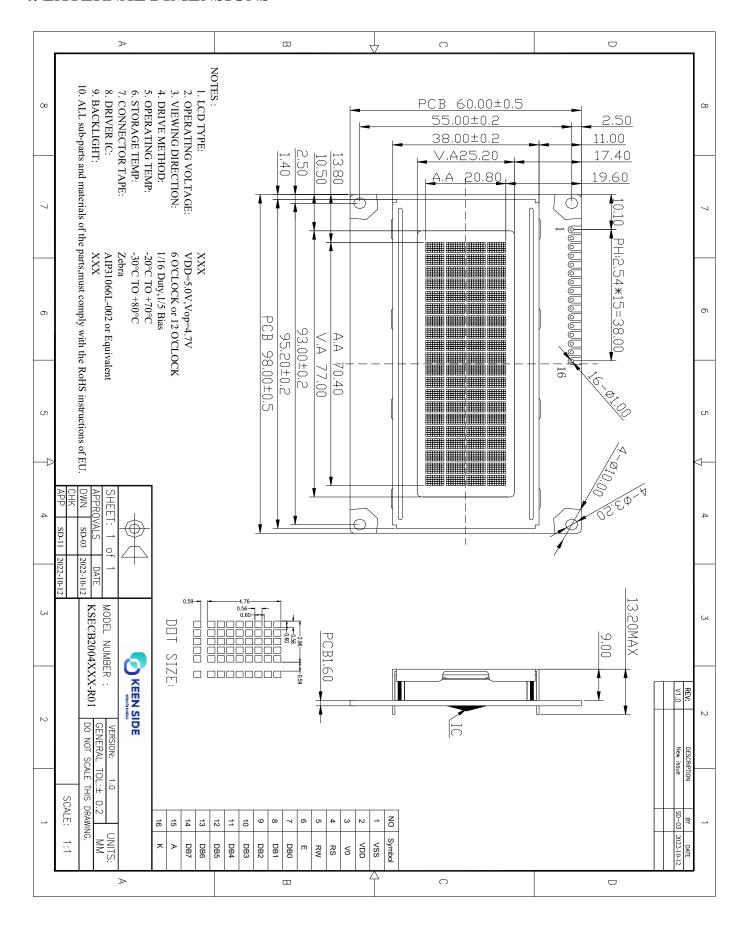
3. MECHANICAL SPECIFICATIONS

• Outline Dimensions : 98.00(W) x 60.00(L) x 13.20(H)(mm)

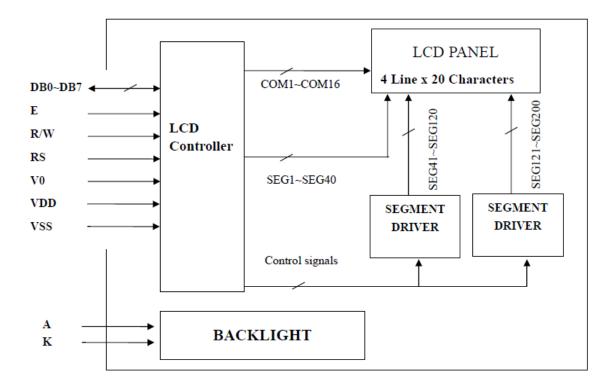
Viewing Area : 77.00 (W) x 25.20(L)(mm)
 Active Area : 70.40 (W) x 20.80 (L)(mm)
 Character Size : 2.96 (W) x 4.76 (L)(mm)
 Character Pitch : 3.55 (W) x 5.35 (L)(mm)
 Dot Size : 0.56 (W) x 0.56 (L)(mm)
 Dot Pitch : 0.60 (W) x 0.60 (L)(mm)

• Weight : TBD

4. EXTERNAL DIMENSIONS



5. BLOC'K DIAGRAM



6. PIN ASSIGNMENT

Pin No.	Symbol	Function						
1	VSS	Ground terminal of module.						
2	VDD	Power terminal of module						
3	V0	Power Supply for liquid crystal drive.						
		Register select						
4	RS	RS = 0···Instruction register						
		$RS = 1 \cdots Data register$						
		Read /Write						
5	R/W	$R/W = 1 \cdots Read$						
		$R/W = 0 \cdots Write$						
6	Е	Read/Write Enable Signal						
7	DB0							
8	DB1							
9	DB2	Bi-directional data bus, data transfer is performed once, thru DB0 to DB7,						
10	DB3	in the case of interface data. Length is 8-bits; and twice, thru DB4 to DB7 in						
11	DB4	the case of interface data length is 4-bits. Upper four bits first then lower						
12	DB5	four bits.						
13	DB6							
14	DB7							
15	A	Anode of Backlight						
16	K	Cathode of Backlight						

7. BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATIONS

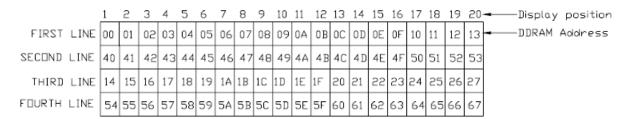
Electrical/Optical Specifications (White)

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
Forward Voltage	Vf	4.8	5.0	5.2	V	If= 15x2 mA	
Reverse Current	Ir			100	μΑ	Vr=5.0 V	
Dominant wave length	X					If= 15x2 mA	
Dominant wave length	Y				ı	11- 13x2 IIIA	
Spectral Line Half width	Δλ				nm	If= 15x2 mA	
Luminous	Lv	70	80		cd/m ²	If= 15x2 mA	

Electrical/Optical Specifications (Yellow-Green)

curear optical specifications (Tenon-Green)											
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS					
Forward Voltage	Vf	4.8	5.0	5.2	V	If= 15x2 mA					
Reverse Current	Ir			100	μΑ	Vr=5.0 V					
Dominant wave length	λD	569	572	575	nm	If= 15x3 mA					
Spectral Line Half width	Δλ		25		nm	If= 15x2 mA					
Luminous	Lv	75	80		cd/m ²	If= 15x2 mA					

8. DISPLAY DATA RAM (DDRAM)



9. MAXIMUM ABSOLUTE POWER RATINGS

Item	Symbol	Standard value	Unit
Power supply voltage(1)	VDD	-0.3~+7.0	V
Power supply voltage(2)	VLCD	VDD-10.0~VDD+0.3	V
Input voltage	VIN	-0.3~VDD+0.3	V
Operating temperature	Topr	-20~+70	$^{\circ}$
Storage temperature	Tstg	-30~+80	$^{\circ}$

^{*}Voltage greater than above may damage to the Circuit.

VDD>V1>V2>V3>V4>V5

10. ELECTRICAL CHARACTERISTICS

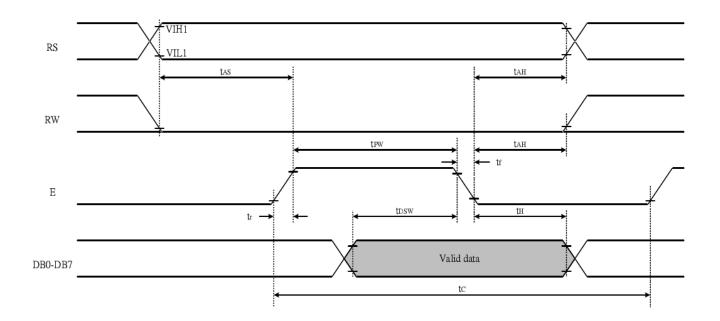
10-1 DC Characteristics

Item	Cymbol	Sta	ndard Va	lue	Test	Unit
Item	Symbol	MIN	TYP	MAX	Condition	UIII
Operating Voltage	VDD	4.8	5.0	5.2		V
Sunalis Cumant	IDD1		TBD	1.0	Ceramic oscillation fosc=250kHz	A
Supply Current	IDD2		TBD	0.6	Resistor oscillation external clock operation fosc=270kHz	mA
LCD Driving Voltage	VLCD	4.5	4.7	5.0	VDD-V0	V

10-2 AC Characteristics

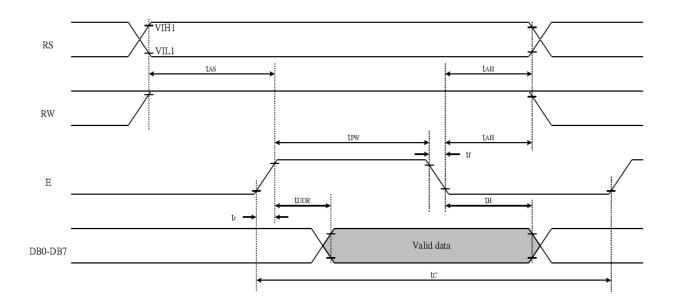
10.2.1 Write mode

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
Enable Cycle Time	t_{C}	1200			ns	Е
Enable Pulse Time	T_{PW}	460			ns	Е
Enable Rise/Fall Time	$T_{R,} T_{F}$			25	ns	Е
Address Set-up Time	Tas	0			ns	R/W,RS,E
Address Hold Time	Тан	10			ns	R/W,RS,E
Data Set-up Time	$T_{ m DSW}$	80			ns	DB0~DB7
Data Hold Time	T _H	10			ns	DB0~DB7



10.2.2 Read mode

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
Enable Cycle Time	$t_{\rm C}$	1200			ns	Е
Enable Pulse Time	T_{PW}	480			ns	Е
Enable Rise/Fall Time	T _R , T _F			25	ns	Е
Address Set-up Time	T _{AS}	0			ns	R/W,RS,E
Address Hold Time	T_{AH}	10			ns	R/W,RS,E
Data Set-up Time	T_{DDR}			320	ns	DB0~DB7
Data Hold Time	T _H	10			ns	DB0~DB7



11. INSTRUCTION TABLE

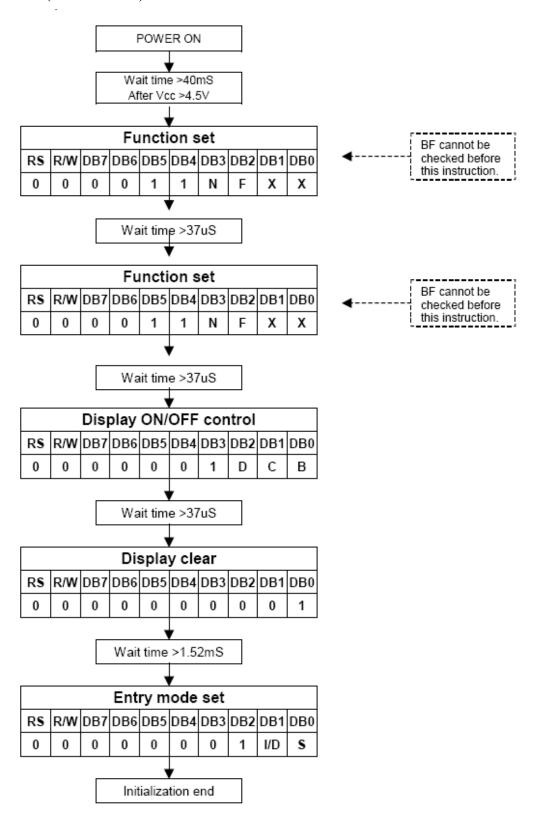
Command	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Execution time (fosc=270KHz)	Remark
Clear Display	0	0	0	0	0	0	0	0	0	1	1.52ms	Write"20H" to DDRAM. And set DDRAM address to "00H" from AC
Return home	0	0	0	0	0	0	0	0	1	Х	1.52ms	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.
Entry mode Set	0	0	0	0	0	0	0	1	I/D	S	37us	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.
Display on/off control	0	0	0	0	0	0	1	D	С	В	37us	D=1: entire display on C=1: cursor on B=1: cursor position on
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	X	X	37us	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.
function Set	0	0	0	0	1	DL	N	F	X	X	37us	DL: interface data is 8/4 bits N: number of line is 2/1 F: font size is 5x11/5x8
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set CGRAM address in address counter
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set DDRAM address in address counter
Read busy flag& address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	0us	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	37us	Write data into internal RAM (DDRAM/CGRAM)
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	37us	Read data from internal RAM (DDRAM / CGRAM)

Note:

Be sure the AIP31066 is not in the busy state (BF=00 before sending an instruction from the MPU to the AIP31066. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to instruction table for the list of each instruction execution time.

12. INITIALIZING BY INSTRUCTION

8-bit interface mode (fosc=270kHz)



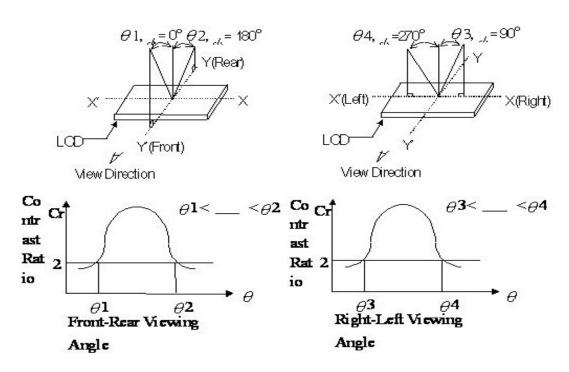
13. CHARACTER GENERATOR ROM

002

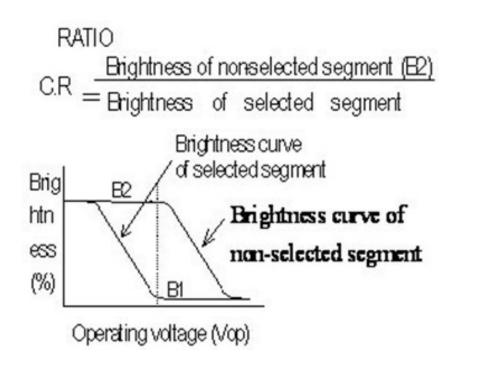
67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															W
0001	(2)															
0010	(3)															
0011	(4)															
0100	(5)															
0101	(6)															
0110	(7)															
0111	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)															
1110	(7)															
1111	(8)															

14. Optical Characteristics

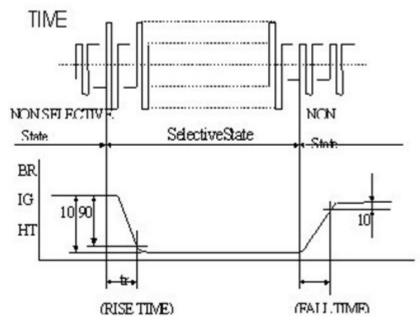
14.1 Definition of Viewing Angle



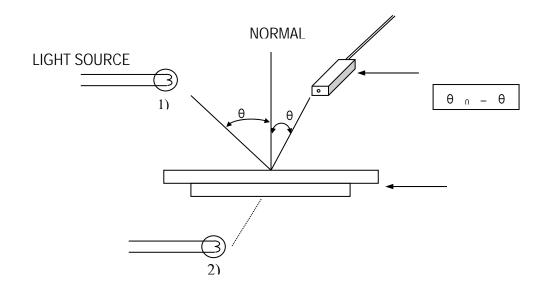
14.2 Definition of Contrast



14.3 Definition of Response



14.4 Measuring Instruments For Elector-optical Characteristics



* Note:

- 1) Light source position for measuring the reflective type of LCD panel;
- 2) Light source position for measuring the transflective / transmissive types of LCD panel.

15. MODULE ACCEPT QUALITY LEVEL (AQL)

15.1 AQL Standard Value: Critical Defect =0.1, Major Defect=0.65; Minor Defect =2.5.

15.2 Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II

16. RELIABILITY TEST

Operating life time: Longer than 75,000 hours

(at room temperature without direct irradiation of sunlight)

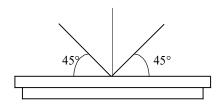
Reliability characteristics shall meet following requirements.

No.	Test Item	Content of Test	Test Condition	
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	+80°C 96H	
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	−30°C 96H	
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	+70°C 96H	
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	−20°С 96Н	
5	High Temperature/ Humidity Storage	Endurance test applying the high temperature and humidity storage for a long time	40°C 90%RH 96H	
6	Temperature Cycle	Endurance test applying the low and high temperature cycle -20°C	-20°C/70°C 5 cycles	
7	Vibration Test (Package State)	Endurance test applying the vibration during transportation	10Hz-55Hz, 50m/s,15min	
8	Shock Test (Package State)	Endurance test applying the shock during transportation	Half-sinewave, 100m/s, 11ms	
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	40 kPa 16 H	

17. Inspection specification

17.1 Visual Inspection

- 1) Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- 2) Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- 3) Inspect the module at 45° right and left, top and bottom.
- 4) Use the optimum viewing angle during the contrast inspection.



16.2 Standard of Appearance Inspection

No.	Item	Criteria					
		Round type: as per following drawing					
		$\Phi = (X+Y)/2$ Acceptable quantity					
				Zone A	Zone B		
			Ф<0.1	Any number			
			0.1<Ф<0.2	2	Any number		
	X		0.2<Ф<0.2		Ally number		
			0.25<Ф	0			
	Black spot	Line type: as per following drawing Acceptable quantity					
		Length	Width	Zone A	Zone B		
1	White spot	_	W≤0.02	Any number			
	Dust	L≤3.0	0.02 <w≤0.03< td=""><td>2</td><td></td></w≤0.03<>	2			
		L≤2.5	0.03 <w≤0.05< td=""><td></td><td>Any number</td></w≤0.05<>		Any number		
		_	0.05 <w< td=""><td>As round type</td><td></td></w<>	As round type			
	L						
		Total acceptable quantity: 3					
2	Polariser scratch	Scratch on	protective film is pern	nitted			
	Polariser scratch	Scratch on polariser: same as No. 1					
3	Polariser bubble	$\Phi = (X+Y)/2$					

		$\bigcirc \bot_{Y}$		Acceptable quantity		
	X		Size	Zone A	Zone B	
		Λ	Ф<0.2	Any num	ber	
			0.2<Ф	2		
			< 0.5		Any numbe	
		Total	0.5<Ф <1.0	1	This name	
		acce	1.0<Ф	0		
		ptabl — e quantity	7: 3			
		4.1 Pin hole or W: segment w Φ = (A+B)/2	n segmented disp idth	lay		
		B .		Acceptab	Acceptable quantity	
			B Wie		Quantity	
		A	A W≤0.4		$\Phi \le 0.2$ and Φ $\le 1/2W$	
		-/ \ /				
		4.2 Pin hole of dot matricular display	on Pin h	egment	tity: 1 defect per under 0.10 mm are	
		ж	≤0.05	Ac	Acceptable quantity	
4	Segment	\Box	50.05	Size		
	deformation	(d	7	a, b<0.	1 Any number	
		7-		(a+b)/2≤	0.1 Any number	
				0.5< ₵	3	
		Total acceptab	le quantity: 7	<1	.0	
		4.3 Segments	dots with different	ent width		
					cceptable	
				a≥b	a/b≤4/3	
		A	В	a <b< td=""><td>a/b>4/3</td></b<>	a/b>4/3	
		4.4 Alignment	layer defect			

SECD2UU4XXX-RU1 SER1ES				vauct Speci	<u> </u>	
		B A		Acceptable quant	ity	
			:	Size		
			Ф	P≤0.4 Any	number	
			0.4	4<Ф	5	
				≤1.0	3	
		M	1.0	0<Ф	3	
				≤1.5	3	
			1.:	5<Ф	2	
				≤2.0	۷	
		Total acceptable quantity: 7				
5	Colour uniformity	Level of sample for approval set as limit sample				
		The backlight colour should correspond to the product specification				
6	Backlight	Flashing and or unlit backlight is not allowed				
		Dust larger than 0.25 mm is not allowed				
		Exposed wire bond pad is not allowed				
7	COB	Insufficient covering with resin is not allowed (wire bond line				
		exposed) Dust or bub				
		No unmelted solder paste s	_		a not	
	D.C.D.	Cold solder joints, missing solder connections, or oxidation are not allowed				
8	PCB	No residue or solder balls on PCB are allowed				
		Short circuits on components are not allowed				
		•			1	
			Acceptable quant		-	
	Tray particles		Size	Quantity	-	
9		On tray	Ф<0.2	Any number	_	
	-		Ф>0.25	4		
		On display	Ф ≥0.25	2	_	
			L = 3	1		

18. LCD MODULES HANDLING PRECAUTIONS

- Please remove the protection foil of polarize before using.
- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- The polarize covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - -Be sure to ground the body when handling the LCD module.
 - -Tools required for assembly, such as soldering irons, must be properly grounded.
 - -To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - -The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

Storage precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0° C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

19. OTHERS

- Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
 - Exposed area of the printed circuit board
 - Terminal electrode sections