



LCD MODULE

MODULE NO.:

KSECB0802XXX-R02 SERIES

Customer:		
Approved by:		
Approved by	Checked by	Prepared by

RECORDS OF REVISION

Part Number	Revision	Revision Content	Revised on
KSECB0802XXX-R02	1.0	First issue	Sep. 18th, 2022

CONTENTS

1. MODULE CLASSIFICATION INFORMATION	4
2. FUNCTIONS & FEATURES	5
3. MECHANICAL SPECIFICATIONS	5
4. EXTERNAL DIMENSIONS	6
5. BLOC`K DIAGRAM	7
6. PIN ASSIGNMENT	7
7. BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATIONS	8
8. DISPLAY DATA RAM (DDRAM)	8
9. MAXIMUM ABSOLUTE POWER RATINGS	8
10. ELECTRICAL CHARACTERISTICS	9
11. INSTRUCTION TABLE	11
12. INITIALIZING BY INSTRUCTION	12
13. CHARACTER GENERATOR ROM	13
15. MODULE ACCEPT QUALITY LEVEL (AQL)	16
16. RELIABILITY TEST	16
17. INSPECTION SPECIFICATION	17
18. LCD MODULES HANDLING PRECAUTIONS	20
10 OTHERS	20

1. MODULE CLASSIFICATION INFORMATION

KSE C B 0802 X X X - R 02

(4)

①KSE: KEEN SIDE electronics

(3)

(2)

② C: Character Type, G: Graphic Type

③ B: COB, G: COG

4 Display Font: 8 * 2

⑤ LCD Mode:

 $\widehat{(1)}$

 $B \rightarrow STN$ -Blue Negative $F \rightarrow FSTN$ Positive

(5) (6) (7)

 $G \rightarrow STN$ Gray Positive $Y \rightarrow STN$ Yellow Green Positive

(8) (9)

6 Backlight Type:

 $N \rightarrow Without backlight$ $A \rightarrow Amber LED backlight$

 $B \rightarrow Blue \ LED \ backlight$ $G \rightarrow Green \ LED \ backlight$

R→ Red LED backlight W→ Withe LED backlight

Y→ Yellow-Green LED backlight

7 LCD Polarize Type/Temperature range/View direction :

D→ Transflective, W.T, 12:00 E→ Transmissive, W.T, 6:00

P→ Reflective, W. T, 6:00 Q→ Transmissive, W.T, 12:00

Z→ Transflective, W.T, 6:00

(8) Character Bank:

A→ English / Japan B→ English / European

R→ English / Cyrillic / Portuguese / Russian

 $T \rightarrow English / Russian \qquad G \rightarrow Hebrew$

9 Model serials no.:

KSECB0802GYZ-R02 SERIES

2. FUNCTIONS & FEATURES

• KSECB0802XXX-R02 Series LCD type:

• Display Contents : 8 * 2 Characters (5*8 dots)

Driving Scheme : 1/16 Duty; 1/5Bias
 Driver IC : AIP31066L-002
 Interface : 4/8-Bit Parallel
 Operating Temperature : -20°C - +70°C
 Storage Temperature : -30°C - +80°C

• RoHS Compliant

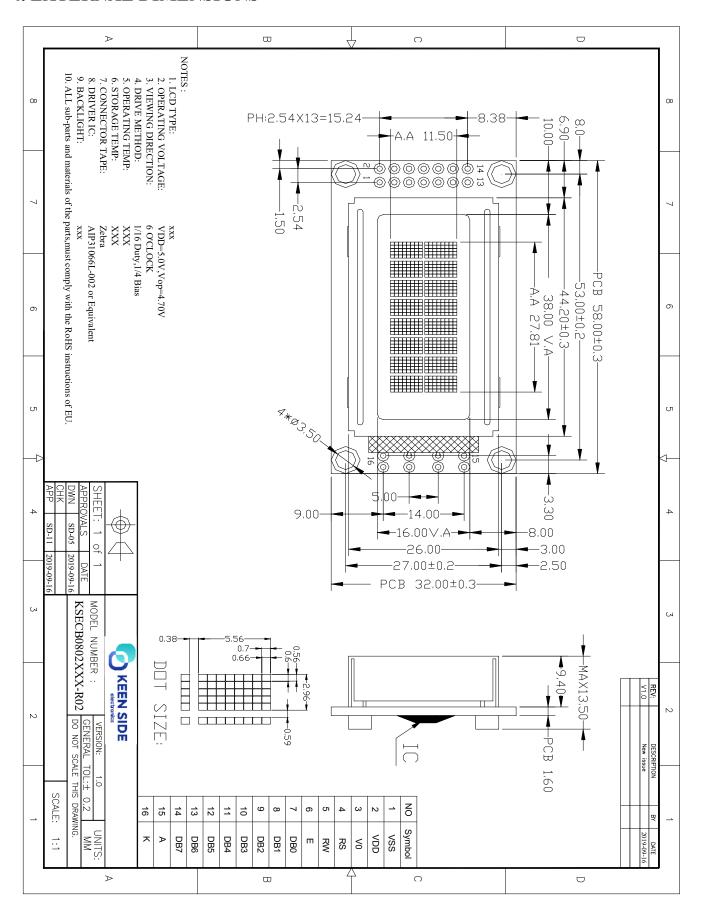
3. MECHANICAL SPECIFICATIONS

• Outline Dimensions : 58.00(W) x 32.00(L) x 13.50(H)(mm)

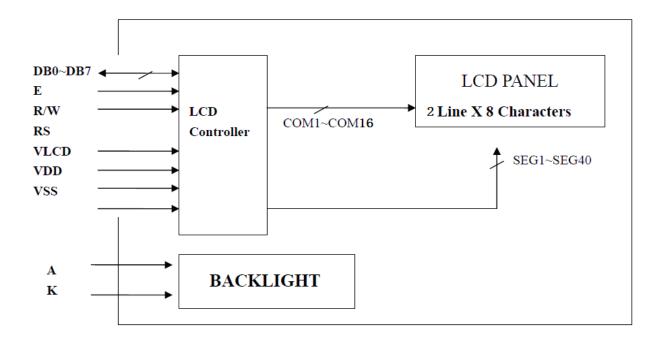
Viewing Area
 38.00 (W) x 16.00(L)(mm)
 Active Area
 27.81 (W) x 11.50 (L)(mm)
 Character Size
 2.96 (W) x 5.56 (L)(mm)
 Character Pitch
 3.55 (W) x 5.94 (L)(mm)
 Dot Size
 0.56 (W) x 0.66 (L)(mm)
 0.60 (W) x 0.70 (L)(mm)

• Weight : TBD

4. EXTERNAL DIMENSIONS



5. BLOC'K DIAGRAM



6. PIN ASSIGNMENT

Pin No.	Symbol	Function						
1	VSS	Ground terminal of module.						
2	VDD	Power terminal of module						
3	V0	Power Supply for liquid crystal drive.						
4	RS	Register select RS = 0···Instruction register RS = 1···Data register						
5	R/W	Read /Write $R/W = 1 \cdots Read$ $R/W = 0 \cdots Write$						
6	Е	Read/Write Enable Signal						
7	DB0							
8	DB1							
9	DB2	Bi-directional data bus, data transfer is performed once, thru DB0 to						
10	DB3	DB7, in the case of interface data. Length is 8-bits; and twice, thru DB4						
11	DB4	to DB7 in the case of interface data length is 4-bits. Upper four bits first						
12	DB5	then lower four bits.						
13	DB6							
14	DB7							
15	A	Anode of Backlight						
16	K	Cathode of Backlight						

7. BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATIONS

Electrical/Optical Specifications (White)

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
Forward Voltage	Vf	4.8	5.0	5.2	V	If= 15 mA
Reverse Current	Ir			100	μΑ	Vr=5.0 V
D	X	0.26		0.30		If= 15 mA
Dominant wave length	Y	0.26		0.30	ı	II- 13 IIIA
Spectral Line Half width	Δλ				nm	If= 15 mA
Luminous	Lv	70			cd/m ²	If= 15 mA

Electrical/Optical Specifications (Yellow-Green)

neeti ean Optical Specifications (Tenow Green)										
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS				
Forward Voltage	Vf	4.8	5.0	5.2	V	If= 15 mA				
Reverse Current	Ir			100	μΑ	Vr=5.0 V				
Dominant wave length	λD	569	572	575	nm	If= 15 mA				
Spectral Line Half width	Δλ		-		nm	If= 15 mA				
Luminous	Lv	70			cd/m ²	If= 15 mA				

8. DISPLAY DATA RAM (DDRAM)

	1	2	3	4	5	6	7	8	▼ DISPLAY POSITION
FIRST LINE	00	01	02	03	04	05	06	07	→DDRAM ADDRESS
SECOND LINE	40	41	42	43	44	45	46	47	

9. MAXIMUM ABSOLUTE POWER RATINGS

Item	Symbol	Standard value	Unit
Power supply voltage(1)	VDD	-0.3~+7.0	V
Power supply voltage(2)	VLCD	VDD-10.0~VDD+0.3	V
Input voltage	VIN	-0.3~VDD+0.3	V
Operating temperature	Topr	-20~+70	${\mathbb C}$
Storage temperature	Tstg	-30~+80	${\mathbb C}$

^{*}Voltage greater than above may damage to the Circuit.

10. ELECTRICAL CHARACTERISTICS

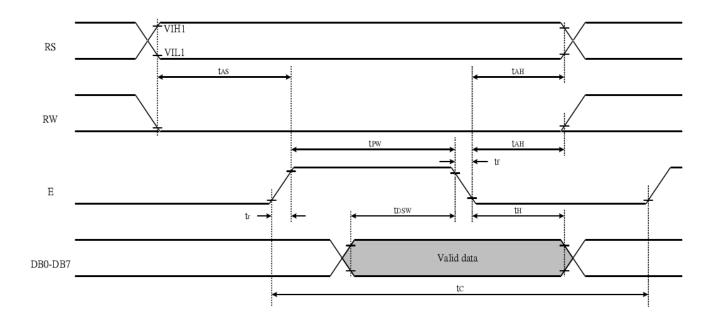
10-1 DC Characteristics

Item	Cymbol	St	andard Va	lue	Test	Unit
nem	Symbol	MIN	TYP	MAX	Condition	Unit
Operating Voltage	V_{DD}	4.8	5.0	5.2		V
	I_{DD1}		TBD	1.0	Ceramic oscillation fosc=250kHz	
Supply Current	I_{DD2}		TBD	0.6	Resistor oscillation external clock operation fosc=270kHz	mA
LCD Driving Voltage	VLCD	4.5	4.7	4.9	V _{DD} -V ₀	V

10-2 AC Characteristics

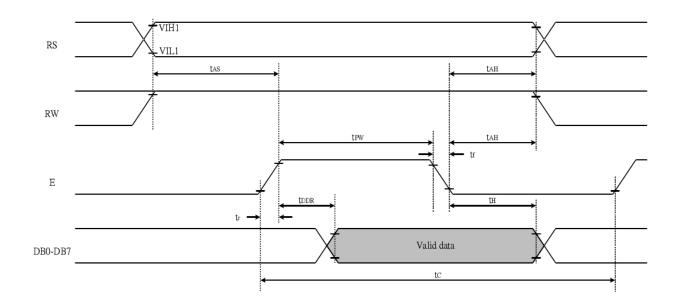
10.2.1 Write mode

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
Enable Cycle Time	t _C	1200			ns	Е
Enable Pulse Time	T_{PW}	460			ns	Е
Enable Rise/Fall Time	$T_{R_{,}}T_{F}$			25	ns	Е
Address Set-up Time	T _{AS}	0			ns	R/W,RS,E
Address Hold Time	T _{AH}	10			ns	R/W,RS,E
Data Set-up Time	T_{DSW}	80			ns	DB0~DB7
Data Hold Time	T_{H}	10			ns	DB0~DB7



10.2.2 Read mode

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
Enable Cycle Time	$t_{\rm C}$	1200			ns	E
Enable Pulse Time	T_{PW}	480			ns	Е
Enable Rise/Fall Time	$T_{R_{,}}$ T_{F}			25	ns	Е
Address Set-up Time	T_{AS}	0			ns	R/W,RS,E
Address Hold Time	Тан	10			ns	R/W,RS,E
Data Set-up Time	T_{DDR}			320	ns	DB0~DB7
Data Hold Time	T_{H}	10			ns	DB0~DB7



11. INSTRUCTION TABLE

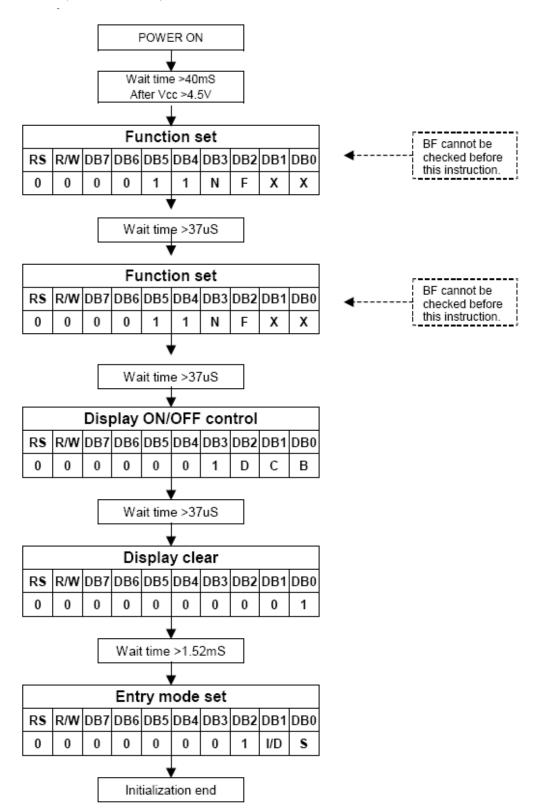
Command	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB3	DB2	DB1	DB0	Execution time (fosc=270KHz	Remark
Clear Display	0	0	0	0	0	0	0	0	0	1	1.52ms	Write"20H" to DDRAM. And set DDRAM address to "00H" from AC
Return home	0	0	0	0	0	0	0	0	1	х	1.52ms	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.
Entry mode Set	0	0	0	0	0	0	0	1	I/D	S	37us	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.
Display on/off control	0	0	0	0	0	0	1	D	С	В	37us	D=1: entire display on C=1: cursor on B=1: cursor position on
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	х	х	37us	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.
function Set	0	0	0	0	1	DL	N	F	х	х	37us	DL: interface data is 8/4 bits N: number of line is 2/1 F: font size is 5x11/5x8
Set CGRAM address	0	0	0	1	AC 5	AC 4	AC3	AC2	AC1	AC0	37us	Set CGRAM address in address counter
Set DDRAM address	0	0	1	AC 6	AC 5	AC 4	AC3	AC2	AC1	AC0	37us	Set DDRAM address in address counter
Read busy flag& address	0	1	BF	AC 6	AC 5	AC 4	AC3	AC2	AC1	AC0	0us	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	37us	Write data into internal RAM (DDRAM/CGRAM)
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	37us	Read data from internal RAM (DDRAM / CGRAM)

Note:

Be sure the AIP31066 is not in the busy state (BF=00 before sending an instruction from the MPU to the AIP31066. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to instruction table for the list of each instruction execution time.

12. INITIALIZING BY INSTRUCTION

8-bit interface mode (fosc=270 kHz)



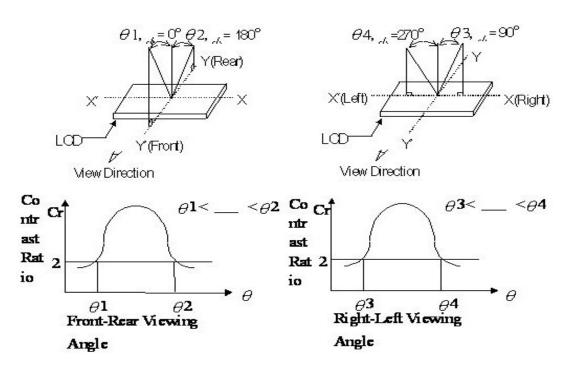
13. CHARACTER GENERATOR ROM

002

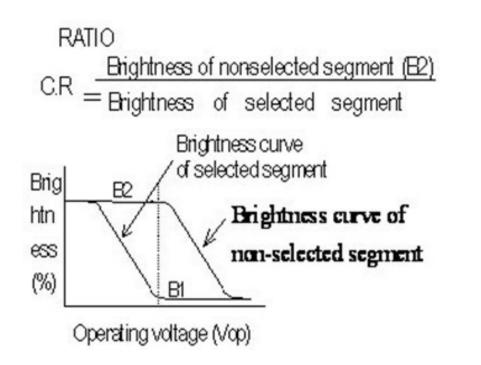
67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															×
0001	(2)												Ш	i		×
0010	(3)						b						1	u		×
0011	(4)		#	×			C.	88,			W	B	ы		a	14
0100	(5)		*	×.			d	ŧ.						*		
0101	(6)		×					W						341		
0110	(7)		8.			W		W			×	**	Ю	*		
0111	(8)					W	8	W					93	ı		H
1000	(1)		×			×		×								
1001	(2)		3									Ø				
1010	(3)		*			X		×			0					4
1011	(4)				×									H		*
1100	(5)													H	ü	M
1101	(6)						×							H		8
1110	7)															
1111	(8)							85								

14. Optical Characteristics

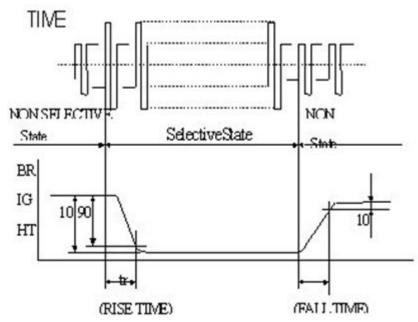
14.1 Definition of Viewing Angle



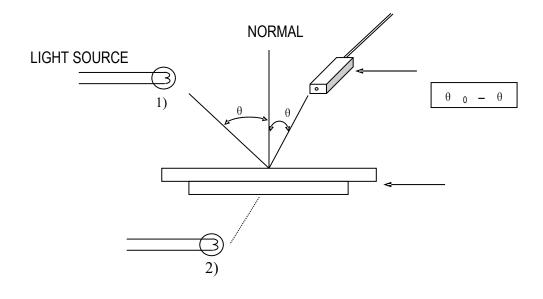
14.2 Definition of Contrast



14.3 Definition of Response



14.4 Measuring Instruments For Elector-optical Characteristics



* Note:

- 1) Light source position for measuring the reflective type of LCD panel;
- 2) Light source position for measuring the transflective / transmissive types of LCD panel.

15. MODULE ACCEPT QUALITY LEVEL (AQL)

15.1 AQL Standard Value: Critical Defect =0.1, Major Defect=0.65; Minor Defect =2.5.

15.2 Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II

16. RELIABILITY TEST

Operating life time: Longer than 50,000 hours

(at room temperature without direct irradiation of sunlight)

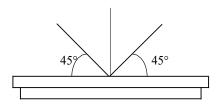
Reliability characteristics shall meet following requirements.

No.	Test Item	Content of Test	Test Condition	
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	+80°C 96H	
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	−30°C 96H	
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	+70°C 96H	
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	−20°C 96H	
5	High Temperature/ Humidity Storage	Endurance test applying the high temperature and humidity storage for a long time	40°C 90%RH 96H	
6	Temperature Cycle	Endurance test applying the low and high temperature cycle -20°C 25°C 70°C 25°C 30min 5min 30min 5min 1 cycle	-20°C/70°C 5 cycles	
7	Vibration Test (Package State)	Endurance test applying the vibration during transportation	10Hz - 55Hz, 50m/s,15min	
8	Shock Test (Package State)	Endurance test applying the shock during transportation	Half-sinewave, 100m/s, 11ms	
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	40 kPa 16 H	

17. Inspection specification

17.1 Visual Inspection

- 1) Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- 2) Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- 3) Inspect the module at 45° right and left, top and bottom.
- 4) Use the optimum viewing angle during the contrast inspection.



17.2 Standard of Appearance Inspection

No.	Item		C	riteria			
		. Round type	: as per following drav	wing			
		=(X+Y)/2	Acceptable quanti	tv		
			Size	Zone A	Zone B		
			Ф<0.				
			0.1<Ф<0.2		A 1		
		(/ X ///////	0.2< Ф<0.2	25 1	Any number		
			0.25<Ф	0			
	Black spot	Line type: a	as per following drawi	ng able quantity			
		Length	Width	Zone A	Zone B		
1	White spot	_	W≤0.02	Any number			
		L≤3.0 0	0.02 <w≤0.03< td=""><td>2</td><td></td></w≤0.03<>	2			
	Dust	L≤2.5	0.03 <w≤0.05< td=""><td>2</td><td>Any number</td></w≤0.05<>	2	Any number		
		_	0.05 <w< td=""><td>As round</td><td></td></w<>	As round			
			0.03~W	type			
		L					
			table quantity: 3				
2	Polariser scratch	Scratch on protective film is permitted					
			polariser: same as No.	1			
3	Polariser bubble	$\Phi = (X+Y)$)/2				

Product Specification

SECDOOZG 1Z NO			11044	e Specifica		
		Acceptable quantity				
		Size	Zone A	Zone B		
		Ф<0.2	Any number			
		0.2<Ф	2			
		< 0.5		Any numbe		
	T. 4.1	0.5<Ф	1	Any number		
	Total	<1.0	1			
	acce ptabl	1.0<Ф	0			
	e quant	tity: 3				
	c quant	iity. 3				
	4.1 Pin hole	on segmented displ	av			
	W: segment		J			
			Acceptable qu			
		Wic		Quantity		
		W≤	0.4 Φ≤0			
		////// ///////////////////////////////	Ŧ .0.0	≤1/2W		
			$\Phi \leq 0.2$			
		Total a	$\Phi \le 1/3$ acceptable quantity:			
		Se	egment	i delect per		
	4.2 Pin hole		oles with Φ und	er 0.10 mm are		
	dot ma	a	cceptable			
	display					
	ж	≤0.05	Accepta	able quantity		
4 Segment			Size			
deformati	on Ca	74	a, b<0.1	Any numbe		
	7-	,	(a+b)/2≤0.1	Any numbe		
			0.5<Ф			
			<1.0	3		
	Total accept	table quantity: 7				
	4.3 Segment	ts / dots with differe	nt width			
			Accept	able		
			a≥b	a/b≤4/3		
		<u>B</u>	a <b< td=""><td>a/b>4/3</td></b<>	a/b>4/3		
	A	ન 				
	4.4 Alignme	ent layer defect				
	$\Phi = (A+B)$	-				

Product Specification

ECB08	02GYZ-R02 S	ERIE	5	P	roduct	Specific		
		~ >	BAX THA		,			
					Acceptab	le quantity		
					Size			
				\sim	Þ≤0.4	Any numb		
		Δ,		0.	4<Ф	5		
				or	≤1.0	5		
		≥ <u> </u>		1.	Ф>0	3		
			c		≤1.5	3		
			<u>-</u> + - -	1.	.5<Ф	2		
					≤2.0			
		Tota	al acceptable quanti	ty: 7				
5	Colour uniformity	Lev	el of sample for app	roval set as limit sa	mple			
	-		-	specification				
6	Backlight	Flashing and or unlit backlight is not allowed						
		Dust larger than 0.25 mm is not allowed						
_		-	osed wire bond pad					
7	COB	Insufficient covering with resin is not allowed (wire bond line						
		exposed) Dust or bubble on the resin are not allowed No unmelted solder paste should be present on PCB						
			-	•		ation are not		
8	PCB	Cold solder joints, missing solder connections, or oxidation are not allowed						
8	ТСБ	No residue or solder balls on PCB are allowed						
			rt circuits on compo					
			1	Acceptable quan				
				Size		antity		
				Φ<0.2		number		
9	Tray particles		On tray	Φ>0.25	Ally I	4		
				$\Phi \geqslant 0.25$		2		
			On display	L=3		1		

18. LCD MODULES HANDLING PRECAUTIONS

- Please remove the protection foil of polarizer before using.
- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - -Be sure to ground the body when handling the LCD module.
 - -Tools required for assembly, such as soldering irons, must be properly grounded.
 - -To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - -The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

■ Storage precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent—static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below

 $0 \,^{\circ}$ C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

19. OTHERS

- Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
 - Exposed area of the printed circuit board
 - Terminal electrode sections